

A NOVEL CORDIC CIRCUITABSTRACT

The CORDIC circuit of this invention comprises a buffer memory to record a plurality or a plurality of group of coordinate values; a phase selector to determine a 5 rotation direction according to values recorded in said buffer memory; a rotation calculator to rotate an input coordinate for a predetermined angle and to calculate resulting coordinate value after such rotation; a rotation counter to count number of rotation being made to said input coordinate; and an angle accumulator to accumulate total rotation angle being made to said input coordinate according to value recorded 10 by said rotation counter. The CORDIC circuit of this invention may further comprise a bit selector to shift bits of said input coordinate.

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